

| L Number | Hits | Search Text | DB | Time stamp |
|-----------------|-------------|--|---|-----------------------------|
| 1 | 2485 | ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:27 |
| 2 | 371 | ((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register)) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:26 |
| 3 | 120 | ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:29 |
| 4 | 106 | 711/\$.ccls. and (((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) and (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:29 |
| 5 | 946 | (memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:28 |
| 6 | 102 | (((storage cache memory) adj4 (controller logic)) and way and ((evict\$3 victim) near3 (line entry buffer queue register))) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag) and ((stor\$3 writ\$3) adj3 back)) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:29 |

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|-----------------|-------------|--|---|-----------------------------|
| 8 | 7366 | ((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 16:53 |
| 9 | 144 | ((memory storage) near3 (bank module sector block)) and ((victim eviction) near4 (line entry cache buffer queue register)) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag)) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:05 |
| 10 | 33 | (decoder deserializer) and (((memory storage) near3 (bank module sector block)) and ((victim eviction) near4 (line entry cache buffer queue register)) and ((memory storage) near3 (module bank sector) and ((cache storage memory) with tag))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:05 |
| 11 | 452 | (sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field)) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:04 |
| 12 | 322 | (decoder deserializer) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:05 |
| 13 | 374 | ((memory storage) near3 (bank module sector block)) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:06 |
| 14 | 255 | ((memory storage) near3 (bank module sector block)) and ((decoder deserializer) and ((sequencer serializer) and (((memory cache storage) adj4 (controller logic circuit)) same (cache memory storage) near4 (index tag field)))) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 17:06 |

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|-----------------|-------------|---|---|-----------------------------|
| 1 | 49 | ((system memory storage) adj3 (controller logic circuit)) with ((tag index field) adj3 (buffer array register))) same ((memory storage) adj3 (bank module sector block)) | USPAT; US-PGPUB; EPO; JPO; DERWENT | 2003/09/04 19:54 |